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**** CONTINUING DATA *******
 This appln claims benefit of 60/272,064 03/01/2001

**** FOREIGN APPLICATIONS *******

IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** SMALL ENTITY **

** 03/26/2002

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|---|--|-------------------------------|-----------------------|-----------------------------|
| Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no | STATE OR COUNTRY CA | SHEETS DRAWING 22 | TOTAL CLAIMS 83 | INDEPENDENT CLAIMS 11 |
| 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance | EXAMINER'S SIGNATURE <i>[Signature]</i> | INITIALS <i>[Initials]</i> | | |
| Verified and Acknowledged | | | | |

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TITLE

Method and apparatus for diagnosing failures in an integrated circuit using design-for-debug (DFD) techniques